

10BASE-T Isolation Modules

Dual Port SMD or DIL Package



US Patent Numbers

5,656,985 6,297,721 B1
 6,297,720 B1 6,320,489 B1
 6,344,785 B1 6,662,431 B1

Product Features:

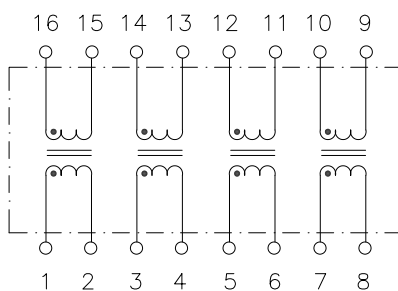
- Standard series of Dual Port Isolation Modules to meet the isolation requirements of IEEE 802.3
- Designed specifically for use with the “filterless” repeater / switch chips and “stacked” RJ45 connectors.
- Non-standard turns ratios are also available
- SMD parts include patented open frame construction
- 1500Vrms hi-pot

Part Number	Pckg	Turns Ratio $\pm 3\%$		PRI OCL (μH)	PRI DCR (Ω max)	PRI LL (μH max)	Cw/w (pF max)	Temp Range
		PRI/SEC PRI 1-2, 3-4, 5-6, 7-8						
TD01-1006L1RL	DIL	1:1, 1:1, 1:1, 1:1		100 Typ.	0.3	0.3	12	0 to +70°C
TG01-1006N2RL	SMD	1:1, 1:1, 1:1, 1:1		100 Typ.	0.3	0.3	12	0 to +70°C
TD54-1006L1RL	DIL	1:1.41, 1:1.41, 1:1.41, 1:1.41		140 Typ.	0.4	0.4	15	0 to +70°C
TG54-1006N2RL	SMD	1:1.41, 1:1.41, 1:1.41, 1:1.41		140 Typ.	0.4	0.4	15	0 to +70°C
TD55-1406L1RL	DIL	1:1, 1:1.41, 1:1.41, 1:1		140/140 Typ.	0.4/0.4	0.4	12	0 to +70°C
TG55-1406N2RL	SMD	1:1, 1:1.41, 1:1.41, 1:1		140/140 Typ.	0.4/0.4	0.4	12	0 to +70°C
TD83-2006L1RL	DIL	1:1, 1:2, 1:2, 1:1		200/50 Min.	0.5/0.35	0.15	11	-40 to +85°C
TG83-2006N2RL	SMD	1:1, 1:2, 1:2, 1:1		200/50 Min.	0.5/0.35	0.15	11	-40 to +85°C

Notes:

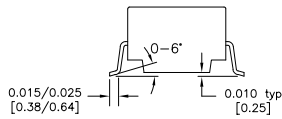
1. Please contact the factory or local representative for individual datasheets or additional information
2. Contact factory for “LF” lead free availability

Schematic



Reinforced 10BASE-T Isolation Modules

Package N2

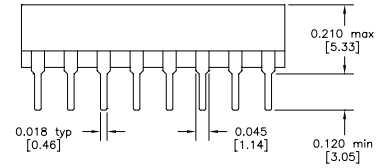
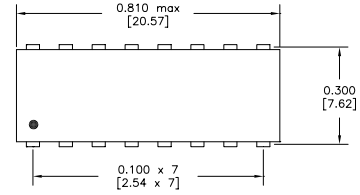


Dimensions: inches [mm]
Co-Planarity: 0.004 [0.10]



Recommended
Solder Pad Layout

Package L1



Dimensions: inches [mm]



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